

structure in such a way that data from message MA and data from a message at input port B enter output port X concurrently.

A1
46.⁵¹ (New) An interconnect structure in accordance with claim 6, wherein the logic L responds to said request by setting a time for the input port P to begin sending message MA into the interconnect structure.

47.⁵² (New) An interconnect structure in accordance with claim 1, wherein the message MA is subdivided into segments and a decision to inject a plurality of the segments of MA depends at least in part on the arrival of one or more messages at input port B.
